

HX432C16FB3/4

4GB 512M x 64-Bit DDR4-3200 CL16 288-Pin DIMM



SPECIFICATIONS

CL(IDD)	17 cycles
Row Cycle Time (tRCmin)	45.75ns(min.)
Refresh to Active/Refresh Command Time (tRFCmin)	260ns(min.)
Row Active Time (tRASmin)	32ns(min.)
UL Rating O	94 V - 0
Operating Temperature	0° C to +85° C
Storage Temperature	-55° C to +100° C
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DESCRIPTION

HyperX HX432C16FB3/4 is a 512M x 64-bit (4GB) DDR4-3200 CL16 SDRAM (Synchronous DRAM) 1Rx8, memory module, based on eight 512M x 8-bit FBGA components per module. Each module kit supports Intel® Extreme Memory Profiles (Intel® XMP) 20. Each module has been tested to run at DDR4-3200 at a low latency timing of 16-18-18 at 1.35V. The SPDs are programmed to at DEC standard latency DDR4-2400 timing of 17-17-17 at 1.2V Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

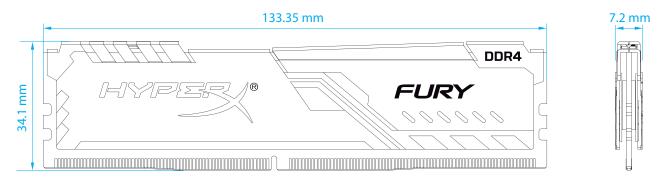
FACTORY TIMING PARAMETERS

- Default (JEDEC): DDR4-240002L17-17-17 @1.2V
- XMP Profile #1:
- DDR4-3200 CL16-18-18 @1.35V
- XMP Profile #2: DDR4-3000 CL15-17-17 @1.35V

FEATURES

- Power Suppry: VDD = 1.2V Typical
- VDDQ 1.2V Typical
- VP = 2.5V Typical
- VDDSPD = 2.2V to 3.6V
 - On-Die termination (ODT)
 - 16 internal banks; 4 groups of 4 banks each
 - Bi-Directional Differential Data Strobe
 - 8 bit pre-fetch
 - Burst Length (BL) switch on-the-fly BL8 or BC4(Burst Chop)
 - Height 1.3425" (34.1mm), w/heatsink

MODULE WITH HEAT SPREADER



MODULE DIMENSIONS O Ŷ Mil measurements are in millimeters. (Tolera ces on all dimensions are ±0.12 unless otherwise specified) 133.35 129.55 2.10±0.15 3.00 Detail A Detail B Detail D etail F Detail C 5 .70±0. 64.60 56.10 22 94

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FOR MORE INFORMATION, GO TO HYPERXGAMING.COM

All Kingston products are tested to meet our published specifications. Some motherboards or system configurations may not operate at the published HyperX memory speeds and timing settings. Kingston does not recommend that any user attempt to run their computers faster than the published speed. Overclocking or modifying your system timing may result in damage to computer components.

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